

CLAIMS:

1. A data carrier (1), which is designed to modulate a carrier signal (CS) that can be received in a contactless manner, and

which is equipped with transmission means (2), designed to transmit the carrier signal (CS), and

5 which is equipped with an electrical circuit (3),

which circuit (3) is equipped with at least one terminal (4, 5), to which terminal (4, 5) the transmission means (2) is connected and via which terminal (4) the carrier signal (CS) can be fed to the circuit, and

10 which circuit (3) is equipped with a data signal source (9) designed to generate and emit a data signal (DS), and

which circuit (3) is equipped with modulation means (11) designed to receive the data signal (DS) and, using the data signal (DS), to modulate the carrier signal (CS) occurring at the at least one terminal, and to generate an amplitude-modulated signal (S), in which amplitude-modulated signal (S) signal edges (SL) occur,

15 characterized in that signal-edge influencing means (12) is provided, which is designed to influence the slope characteristic of the signal edges (SL) in the amplitude-modulated signal (S).

2. A data carrier (1) as claimed in claim 1, characterized in that the signal-edge
20 influencing means (12) is realized by filtration means.

3. A data carrier (1) as claimed in claim 2, characterized in that the filtration means is provided between the data signal source (9) and the modulation means (11) and designed to filter the data signal (DS) that can be emitted from the data signal source (9) to
25 the modulation means (11).

4. A data carrier (1) as claimed in claim 2, characterized in that the filtration means is formed by a low-pass filter.

5. A circuit (3) for a data carrier (1) which is designed to modulate a carrier signal (CS) that can be received in a contactless manner, and which is equipped with transmission means (2) to transmit the carrier signal (CS), which circuit (3) is equipped with at least one terminal (4, 5), to which terminal (4, 5) the transmission means (2) can be
5 connected, and via which terminal (4) the carrier signal (CS) can be fed to the circuit, and which circuit (3) is equipped with a data signal source (9) designed to generate and emit a data signal (DS), and which circuit (3) is equipped with modulation means (11) designed to receive the data signal (DS) and, using the data signal (DS), to modulate the carrier signal (CS)
10 occurring at the at least one terminal, and to generate an amplitude-modulated signal (S), in which amplitude-modulated signal (S) signal edges (SL) occur, characterized in that signal-edge influencing means (12) is provided, which is designed to influence the slope characteristic of the signal edges (SL) in the amplitude-modulated signal (S).

15 6. A circuit (3) as claimed in claim 5, characterized in that the signal-edge influencing means (12) is realized by filtration means.

7. A circuit (3) as claimed in claim 6, characterized in that the filtration means is
20 provided between the data signal source (9) and the modulation means (11) and designed to filter the data signal (DS) that can be emitted from the data signal source (9) to the modulation means (11).

8. A circuit (3) as claimed in claim 6, characterized in that the filtration means is
25 formed by a low-pass filter.

9. A circuit (3) as claimed in claim 1, characterized in that the circuit (3) is realized as an integrated circuit.